

PATENT

In the United States Patent and Trademark Office

Applicant: SUTERA et al.

Applicant's Reference: SUNMP099

Application No: 09/430,350

Filed: October 29, 1999

For: METHOD FOR REDUCING NOISE IN
INTEGRATED CIRCUIT LAYOUTS

Examiner: JONES, H.

Art Unit: 2128

Date: October 29, 2004

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 29, 2004.

Signed:

Neely D. Entwistle

Separate Letter to the Official Draftsperson


Commissioner for Patents
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowability dated July 30, 2004, indicating the drawings submitted on June 5, 2002 have been accepted, Applicants respectfully point out that although the **formal** drawings were submitted in the above-referenced application on December 3, 2002, the Notice of Allowability indicates drawings submitted on June 5, 2002 have been accepted. Accordingly, to ensure the subject application issues with formal drawings, the Applicants are re-submitting the formal drawings for the above-referenced case. Applicants hereby enclose four (4) sheets of formal drawings (FIGs. 1, 2, 3, and 4), for the above-referenced case, which are identical to the formal drawings previously filed on December 3, 2002. If the draftsperson has any questions concerning these drawings, please contact the undersigned at the number set forth below.

Applicants believe that no fees are due in connection with the filing of the Formal Drawings. However, if it is determined that any fees are due in connection with the filing of these drawings, the Commissioner is authorized to charge such fees to deposit account 50-0805 (Order No. SUNMP099).

Respectfully submitted,
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